

Siddaganga Institute of Technology  
Department of Electrical & Electronics Engineering  
Time-Table 2022-2023(ODD SEMESTER)

With effect from 31.10.2022

Class: 7<sup>th</sup> Semester E&EE

Days/Time	8 to 9 AM	9 to 10 AM	10 to 10.30 AM	10.30 to 11.30 AM	11.30 AM to 12.30 PM	12.30 to 2 PM	2 to 3 PM	3 to 4 PM	4 to 5 PM
Monday	APE		BREAK	PSS Lab - B1		LUNCH BREAK	PS-III	ACT	PS-III
Tuesday	OE			PSS Lab - B2			ACT	APE	DSP
Wednesday							Industrial Training		
Thursday	OE	DSP		APE	PS-III				
Friday	DSP	PS-III		ACT	DSP		PSS Lab - B3		
Saturday	OE			PSS Lab - B4					

	Subject	Faculty
OE	Open Elective-III	
PS-III	Power Systems-III (7REE01)	P K Chandrashekhar
DSP	Digital Signal Processing and Architecture (7REE02)	Dr. H. S. Sridhar
APE	Advanced Power Electronics (REEE11)	Rajesh Uppara
ACT	Advanced Control Theory (REEE12)	B.A. Sridhar

Head of the Department

Principal